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UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/955,632
Filing Date September 18, 2001
Inventor Kunal R. Parekh et al.
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Unassigned
Attorney's Docket No. MI22-1816
TITLE: Capacitors, DRAM Arrays, Monolithic Integrated Circuits, And Methods of
Forming Capacitors, DRAM Arrays, And Monolithic Integrated Circuits

Assistant Commissioner for Patents
Washington, D. C. 20231
Attention: Official Draftsman

SUBSTITUTE DRAWING REQUEST

Please enter the enclosed substitute drawings in the above-referenced application in place of drawings originally filed. The content of the drawings are identical to those now on file in this application.

Acknowledgment of receipt of the formal drawings and their acceptance into the file is requested.

Respectfully submitted,

Date: Oct 22, 2001

By:

Frederick M. Fliegel, Ph.D.

Reg. No.: 36,138

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Enclosures: 17 Sheets of Formal Drawings, Figs. 1-17.